INDIAN INSTITUTE OF TECHNOLOGY GANDHINAGAR
Research & Development Office

ADVERTISEMENT No.SPON/63038/Advt0381 Dated 23-11-2026

Applications are invited for a temporary position of Project Associate - I at IIT Gandhinagar.

The details are as below:

Name of PI	Madhav Pathak	Department	Electrical Engg
Project title	NSF-MeitY: Fundamental Realizable Limit Vibration Energy Harvesting: Design, Analysis, and Implementation		
Designation	Project Associate - I		
Number of positions	2		
Application Link	http://recruitment.iitgn.ac.in/projectstaff/		
Last date of application submission	03-12-2026		
Consolidated Monthly Remuneration including HRA	(i) ₹37,000/- + HRA to Scholars who are selected through: (a) National Eligibility Tests — CSIR-UGC including Lectureship (Assistant Professorship) or GATE, or (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions. (ii) ₹30,000/- + HRA for others who do not fall under (i) above		
House Rent Allowance per month (If applicable)	30% (If applicable)		
Duration of appointment	1 Year or project completion 10.12.2027 (whichever is earlier)		
Eligibility Norms[1]	☐ Institute norms	☒ Funding Agency norms	
Essential Qualification/Experience as per the norms[1]	Four years Bachelor's Degree in Natural or Agricultural Sciences / Master's Degree in Natural or Agricultural Sciences / BVSc / B.Pharm or Bachelor's degree in Engineering or Technology or Medicine from a recognized University or equivalent *The percentage/grade points with respect to the academic qualification will be a minimum 60% or equivalent grade from graduation onwards and 55% or equivalent grade in class 10th and 12th.*		
Desirable Qualification/Experience	MTech/ME or BTech/BE in Electronics/Electrical or Equivalent Engineering Stream with ● Knowledge of schematic design, PCB layout, component selection, and prototyping. ● Experience in embedded systems, microcontrollers, and firmware development using C/C++ and Python. ● Familiarity with sensors, ADC, UART, SPI, and I²C interfaces. ● Hands-on experience with oscilloscopes, digital multi-meter, function generators, bench power supplies, etc. ● Skills in hardware debugging, circuit testing, soldering, and prototype validation. **OR** ● Proficiency with analog and mixed-signal IC design ● Experience with circuit simulation tools like Cadence Virtuoso or equivalent EDA tools ● Familiarity with open-source circuit design and simulation tools is		

"Merely meeting the minimum eligibility criteria will not guarantee the shortlisting and/or appointment. A higher cutoff level may be used. The Institute reserves all rights, not to call an applicant for a selection test or not to fill up a position through this round of selection process without assigning any reason."

	required. ● Experience of building AI tools is an added advantage ● Experience with IC layout is an added advantage.
Job Description	The role involves embedded systems development, circuit design, PCB layout, and hardware prototyping. Responsibilities include firmware development, sensor interfacing, hardware testing and debugging, soldering using laboratory instruments, and prototype validation, on-site testing for ongoing R&D projects. **OR** The work will involve exploring open-source tools for analog and mixed-signal circuit design, simulation, and layout for low-power IC design. Further, integration of AI for analog IC design automation will be explored.
Contact	Madhav K Pathak madhav.pathak@iitgn.ac.in