INDIAN INSTITUTE OF TECHNOLOGY GANDHINAGAR
Research & Development Office

SPON/62034/Advt0372

Name of PI	Joycee Mekie	Department	Electrical Engineering
Project title	Secure and Energy efficient Mixed domain Compute in memory-based AI accelerator Chip for Edge applications		
Designation	Project Associate - I		
Number of positions	1		
Application Link	http://recruitment.iitgn.ac.in/projectstaff/		
Last date of application submission	November 18, 2026		
Consolidated Monthly Remuneration including HRA	Rs. 25000 OR Rs. 31000 (if GATE qualified		
House Rent Allowance per month (If applicable)	Rs. 6000 or Rs. 7440 (if GATE qualified)		
Duration of appointment	1 year		
Eligibility Norms[1]	☐ Institute norms	☒ Funding Agency norms	
Essential Qualification/Experience as per the norms[1]	Master's Degree in Natural or Agricultural Sciences / MVSc or Bachelor's Degree in Engineering or Technology or Medicine from a recognized University or equivalent. *The percentage/grade points with respect to the academic qualification will be a minimum 60% or equivalent grade from graduation onwards and 55% or equivalent grade in class 10th and 12th.*		
Desirable Qualification/Experience	BTech / BE / MTech / ME degree in Electrical/ Electronics / Electronics and communications/Electronics and Instrumentation/Computer Science and Engineering/Computer Science or related disciplines with at least 60% or equivalent. The candidates with experience of using VLSI EDA tools will be preferred and those with decent coding skills will be preferred. Candidates with good programming skills C/C++/Python, Verilog/System Verilog or equivalent, etc. and candidates with interest in computer architecture and having knowledge of system simulators will be given preference. Students with exposure of Analog circuits, ADCs, DACs, etc., chip tape-out experience will be preferable.		

"Merely meeting the minimum eligibility criteria will not guarantee the shortlisting and/or appointment. A higher cutoff level may be used. The Institute reserves all rights, not to call an applicant for a selection test or not to fill up a position through this round of selection process without assigning any reason."

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Job Description	Applications are invited for ONE Project Associate I position for MEITY sponsored project in C2S. The candidate is expected to have excellent understanding of Digital & Analog Circuits, VLSI design, etc. and should have a exposure to relevant VLSI tools and techniques.
Contact	Prof. Joycee Mekie, Professor, Department of Electrical Engineering, **Email**: joycee@iitgn.ac.in

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