



**Detailed Advertisement for the**

**Recruitment- Junior Research Fellow (JRF)- (01)**

The eligible candidates meeting the following requirements and desiring to be considered for the above mentioned post on purely contract basis for research project entitled “*Implementation of Novel Speed Control Strategies for Photo Voltaic based Permanent Magnet Assisted Synchronous Reluctance Motor (PMaSyRM) Drives in Electric Vehicles*” funded by the Science and engineering research board (SERB), Ministry of Science and Technology, Government of India; at the institute are hereby informed to submit the application with the required details for the offline Interview/tests at the date and time (to be declared after the last date of receiving the offline applications). Candidates having similar previous experience will be preferred. Selected candidate may get the chance to enroll for Ph.D. programme. For any further information, please contact **Dr. Kuniseti V. Praveen Kumar, Dr. Rajasekhareddy Chilipi, Dr. Suresh Lakhimsetty**, through e-mail ([kvpraveenkumar@eed.svnit.ac.in](mailto:kvpraveenkumar@eed.svnit.ac.in), [rsreddy@eed.svnit.ac.in](mailto:rsreddy@eed.svnit.ac.in), and [suresh@eed.svnit.ac.in](mailto:suresh@eed.svnit.ac.in)) or visit [www.svnit.ac.in](http://www.svnit.ac.in).

**Eligibility & Experience:**

- First Class M. E. /M.Tech. in Power Electronics, Power Electronics and Drives, Electrical Engineering, or Allied branches.
- Candidate having experience in the field of above mentioned project, qualified GATE, M.Tech second year (who are at the final stage of dissertation submission) from the reputed institutes (CFTIs) will be given preference.

**Tenure:** Initially 01 Year, extendable based on the performance till the project continues.

**Monthly Emoluments:**

First & Second Year: Monthly, Rs. 31,000.00/- as per the SERB-JRF norms

Third Year (06 Months): Monthly, Rs. 35,000.00/- as per the SERB-SRF norms

**Important Dates**

Last Date to Apply (Online document): 20 March 2024, 17:00 hr.

Last date to receive application in Physical Mode: 25 March 2024, 17.00 hr.

Display of eligible candidates list on Institute Website: 04 April 2024

Tentative date of interview/ test: Third week of April 2024 (**Exact schedule will be displayed on the institute website along with list of eligible candidates**)

**Link for submission of online application form:** <https://forms.gle/kt4zmFbcqWRw7ryaA>

**Address to send the Physical Application form:**

Dr. Kuniseti V. Praveen Kumar,  
EN 321, Department of Electrical Engineering,  
Sardar Vallabhbhai National Institute of Technology, Surat-395007, Gujarat, India

**Interested candidates will be required to note the following:**

- i. The interested candidates will be required to submit their application form along with all essential details and scanned copy of all their original certificates, mark sheets and a recent passport size photograph through the above mentioned link within last date, i.e, 20 March 2024, 17:00 hr. For submission of offline application (see Annexure-I), fill all the necessary details and the application with all enclosures should reach the address specified above by 25/03/2024, 05.00 PM.



- ii.** Candidates need to bring the complete hardcopy set of the application form with xerox copies of all required documents on the day of interview for verification purpose.
- iii.** Candidates are requested to positively mention their e-mail ID and Mobile Number in the Application failing which their candidature may be rejected.
- iv.** The applications received within the last date & time stipulated will be screened by a selection committee, and a list of eligible candidates shortlisted for interview/test will be displayed on the Institute's official website along with the date and time schedule of the interview/test. The Screening Committee will devise its own criteria for shortlisting of candidates for interview. Merely fulfilling main eligibility criteria may not be suitable for eligible to be invited for the interview/test.
- v.** The decision of SVNIT-Surat in all matters relating to eligibility, acceptance, screening, mode of selection will be final and binding on the candidates and no enquiry correspondence will be entertained in this connection from any individual or any agency on behalf of the candidate.
- vi.** The original documents of selected candidates will be verified at the time of Interview, and in the event of any information mentioned in application/CV/testimonials found false or incorrect, their candidature straightway shall be cancelled.
- vii.** No TA/DA will be paid for attending the interview/test.

**Sd/-  
Project Investigators**



**Annexure-I**

**Application form for the post of JRF**

**Application for the post of JRF** at Department of Electrical Engineering, SVNIT, Surat, Gujarat

**Title of project:** Implementation of Novel Speed Control Strategies for Photo Voltaic based Permanent Magnet Assisted Synchronous Reluctance Motor (PMaSyRM) Drives in Electric Vehicles

**For office use only:**

Serial Number:

Eligible for Interview: YES/NO

Checked the Certificates:

Paste self-attested recent photograph

**1. Name:**

**2. Address:**

**3. Date of birth:**

**4. Category (General/SC/ST/OBC):**

**5. Contact details:**

**(i) Email ID:**

**(ii) Mobile No.:**

**6. Educational qualifications:**

| Examination Passed        | Board/University | Subject | Year | Percentage/CGPA | Division/Class |
|---------------------------|------------------|---------|------|-----------------|----------------|
| M.E./M.Tech.              |                  |         |      |                 |                |
| B.Tech.                   |                  |         |      |                 |                |
| 12 <sup>th</sup> /Diploma |                  |         |      |                 |                |
| Other                     |                  |         |      |                 |                |



**7. B. Tech project details:**

**8. M. Tech project details:**

**9. Professional experience:**

| <b>Organization</b> | <b>Post held</b> | <b>From</b> | <b>To</b> | <b>Pay Drawn</b> | <b>Nature of Duties</b> |
|---------------------|------------------|-------------|-----------|------------------|-------------------------|
|                     |                  |             |           |                  |                         |

**10. Publications (If any) (add extra sheet if required):**

**11. Experience relevant to the field of project (as per title of the project):**

**12. Why do you feel that you are the best candidate for this position?**

**13. Do you have interest in joining Ph.D. program? (Yes/No):**

**14. GATE details:**

**15. Additional information if any, which you would like to mention in support of your suitability for the post, (attach separate sheet if necessary)**



**Sardar Vallabhbhai National Institute of Technology, Surat - 395007, Gujarat**

**Department of Electrical Engineering**

**16. Recommendation (Optional):** *(Provide complete details like name, address, email ID and contact number)*

**Declaration:**

I have provided correct information as above and I understand that, if found incorrect, I may be disqualified for the position.

**Date:**

**Place:**

**(Signature of the applicant)**

**Note:** Candidate need to submit their application form along with all essential details and scanned copy of required documents through the following link within the last date. The filled application along with all enclosures should reach the following address by 25/03/2024, 05.00 PM Candidate need to bring the complete hardcopy set of the application form with xerox copies of all required documents on the day of interview/test.

**Address:**

Dr. Kunisetti V. Praveen Kumar,  
EN 321, Department of Electrical Engineering,  
Sardar Vallabhbhai National Institute of Technology, Surat-395007, Gujarat, India